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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of	:	November 6, 2002
H. Takayasu, et al.	:	IBM Corporation
Ser. No. 09/918,986	:	Dept. T81/Bldg 503-3
Filed 7-31-2001	:	P. O. Box 12195
For: Method and Apparatus for Determining the	:	Res. Tri. Park, NC 27709
Performance of Data Processing Device with	:	Art Unit 2863
Unsynchronized Clocks	:	Examiner: Tung S. Lau
	:	
Attorney's docket No. JP920000214US1	:	

**AMENDMENT**

Assistant Commissioner for Patents  
Washington, DC 20231

Sir:

In response to the office action of 21 October 2002 regarding the above-identified application, Applicant submits the following amendment:

**IN THE CLAIMS**

Please amend the claims to read as follows:

1. (Amended twice) A method for determining the performance of a data processing system wherein processing is started by a first data processing device and finished by a second data processing device which may have separate clocks that are not synchronized, the method